

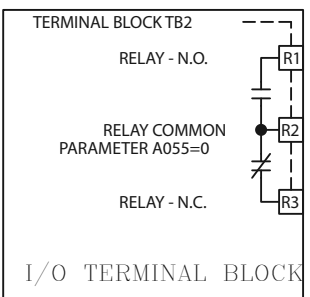
I/O TERMINAL BLOCK	
1	STOP
2	START/RUN FWD P036 = 2 "2-WIRE" P038 = 4 "PRESET FREQ"
3	DIR/RUN REV
4	DIGITAL COMMON
5	DIGITAL IN1 A051 = 4 "PRESET FREQ"
6	DIGITAL IN2 A052 = 4 "PRESET FREQ"
7	DIGITAL IN3
8	DIGITAL IN4
9	OPTO COMMON
11	+24V DC
12	+10V DC
13	0-10V IN
14	ANALOG COMMON
15	4-20mA IN
16	ANALOG OUTPUT
17	OPTO OUTPUT 1
18	OPTO OUTPUT 2
19	RS485 (DSI) SHIELD

SNK
 SRC

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REV.	DATE	BY	ECN NUMBER	DESCRIPTION
01	04-25-08	ARS	1032063	ACAD

DATE	BY	DESCRIPTION
02-25-08	ARS	ACAD
02-25-08	ARS	REFERENCE DWG
02-25-08	ARS	MASTER #420M6



Allen-Bradley
Rockwell Automation

TITLE: SCHEMATIC, POWERFLEX 40 AC DRIVE
 CONTROL LOGIC OPTION 522
 PROJECT: DRIVE

DWG NO: C-349932-2
 SHEET: 15
 NEXT SHEET: 16